

LS XGI Cnet

Supported Series: LS XGT series XGI CPU series with communication module XGL-CH2A

Website: <http://www.lgis.com/>

HMI Setting:

Parameters	Recommended	Options	Notes
PLC type	LS XGI Cnet		
PLC I/F	RS232	RS232/RS485 4W	
Baud rate	115200	9600~115200	
Data bits	8	7, 8	
Parity	None	Even, Odd, None	
Stop bits	1	1	
PLC sta. no.	0	0~32	

Device Address:

Bit/Word	Device type	Format	Range	Memo
B	AW_Bit	DDDDDDh	0 ~ 262143f	
B	IW_Bit	DDD.DD.Dh	0 ~ 127.15.3f	
B	QW_Bit	DDD.DD.Dh	0 ~ 127.15.3f	
B	MW_Bit	DDDDDDh	0 ~ 262143f	
B	RW_Bit	DDDDh	0 ~ 32767f	
B	WW_Bit	DDDDDDh	0 ~ 524287f	
B	FW_Bit	DDDDh	0 ~ 4095f	
B	KW_Bit	DDDDh	0 ~ 8399f	
B	LW_Bit	DDDDh	0 ~ 11263f	
B	NW_Bit	DDDDh	0 ~ 25087f	
B	UW_Bit	DD.DD.DDh	0 ~ 31.15.31f	
B	AX	DDDDDDDD	0 ~ 4194303	
B	IX	DDD.DD.DD	0 ~ 127.15.63	
B	QX	DDD.DD.DD	0 ~ 127.15.63	
B	MX	DDDDDDDD	0 ~ 4194303	
B	RX	DDDDDD	0 ~ 524287	
B	WX	DDDDDDDD	0 ~ 8388607	
B	FX	DDDDD	0 ~ 65535	
B	KX	DDDDDD	0 ~ 134399	

Bit/Word	Device type	Format	Range	Memo
B	LX	DDDDDDDD	0 ~ 1880223	
B	NX	DDDDDD	0 ~ 401407	
B	UX	DD.DD.DDD	0 ~ 31.15.511	
W	AW	DDDDDD	0 ~ 262143	
W	IW	DDD.DD.D	0 ~ 127.15.3	
W	QW	DDD.DD.D	0 ~ 127.15.3	
W	MW	DDDDDD	0 ~ 262143	
W	RW	DDDDD	0 ~ 32767	
W	WW	DDDDDD	0 ~ 524287	
W	FW	DDDD	0 ~ 4095	
W	KW	DDDD	0 ~ 8399	
W	LW	DDDDD	0 ~ 11263	
W	NW	DDDDD	0 ~ 25087	
W	UW	DD.DD.DD	0 ~ 31.15.31	
DW	MD	DDDDDD	0 ~ 131071	
Byte	MB	DDDDDDDD	0 ~ 2097151	
LW	ML	DDDDDD	0 ~ 262143	

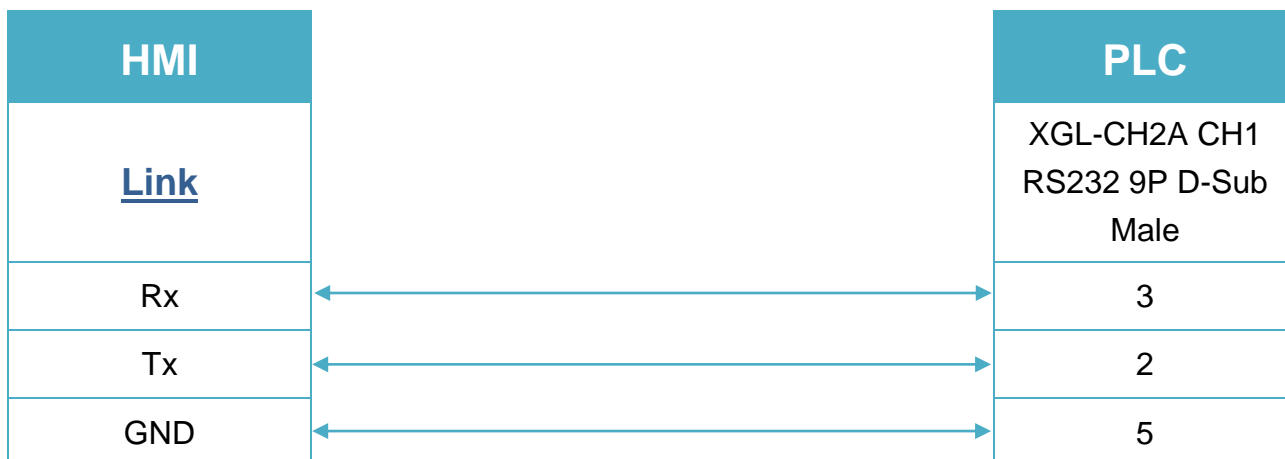
Wiring Diagram:

XGL-CH2A CH1 RS232 9P D-Sub Male

Diagram 1

RS-232

The serial port pin assignments may vary between HMI models, please click the following link for more information.



XGL-CH2A CH2 5P Terminal

Diagram 2

RS-485 4W

The serial port pin assignments may vary between HMI models, please click the following link for more information.

